

Appl. No.: 09/651,924
Amdt. dated February 12, 2004
Reply to Office action of December 3, 2003

Amendments to the Specification:

Please replace the paragraph beginning at page 1, line 9, with the following rewritten paragraph:

This application relates to the following commonly assigned co-pending applications entitled:

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~~"Scan Wheel—An Apparatus And Method For Interfacing A High Speed Scan-Path With A—Slow—Speed Tester Equipment,"~~ Serial No. 09/653,642, filed August 31, 2000, ~~Attorney Docket No. 1662-23700;~~
~~"Priority Rotary Rules For Reducing Network Message Routing Latency And Coherence Dependence Priority Rule,"~~ Serial No. 09/652,322, filed August 31, 2000, ~~Attorney Docket No. 1662-27300;~~ "Speculative Scalable Directory Based Cache Coherence Protocol," Serial No. 09/652,703, filed August 31, 2000, ~~Attorney Docket No. 1662-27400;~~ "Scalable Efficient I/O Port Protocol," Serial No. 09/651,391, filed August 31, 2000, ~~Attorney Docket No. 1662-27600;~~ "Efficient Translation Lookaside Buffer Miss Processing In Computer Systems For Applications Using Large Pages In Systems With A Large Range Of Page Sizes By Eliminating Page Table Level," Serial No. 09/652,552, filed August 31, 2000, ~~Attorney Docket No. 1662-27600;~~ "Fault Containment And Error Recovery Techniques In A Scalable Multiprocessor," Serial No. 09/651,949, filed August 31, 2000, ~~Attorney Docket No. 1662-27700;~~ "Speculative Directory Writes In A Directory Based Cache Coherent CC-Non-Uniform Memory Access Protocol," Serial No. 09/652,834, filed August 31, 2000, ~~Attorney Docket No. 1662-27800;~~ "Special Encoding Of Known Bad Data," Serial No. 09/652,314, filed August 31, 2000, ~~Attorney Docket No. 1662-27900;~~ "Broadcast Invalidate Scheme," Serial No. 09/652,165, filed August 31, 2000, ~~Attorney Docket No. 1662-28000;~~ "Mechanism To Track Keep-All Open Pages Open In A DRAM Memory System," Serial No. 09/652,704, filed August 31, 2000, ~~Attorney Docket No. 1662-28100;~~ "Programmable DRAM Address Mapping Mechanism," Serial No. 09/653,093, filed August 31, 2000, ~~Attorney Docket No. 1662-28200;~~ "Computer Architecture And System For Efficient

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~~Management Of Bi-Directional Bus Mechanism To Enforce Memory Read/Write Fairness, Avoid Tristate Bus Conflicts, And Maximize Memory Bandwidth,~~ Serial No. 09/652,323, filed August 31, 2000, ~~Attorney Docket No. 1662-29200~~; "An Efficient Address Interleaving With Simultaneous Multiple Locality Options," Serial No. 09/652,452, filed August 31, 2000, ~~Attorney Docket No. 1662-29300~~; "A High Performance Way Allocation Strategy For A Multi-Way Associative Cache System," Serial No. 09/653,092, filed August 31, 2000, ~~Attorney Docket No. 1662-29400~~; "Method And System For Absorbing Defects In High Performance Microprocessor With A Large N-Way Set Associative Cache," Serial No. 09/651,948, filed August 31, 2000, ~~Attorney Docket No. 1662-29500~~; "A Method For Reducing Directory Writes And Latency In A High Performance, Directory-Based, Coherency Protocol," Serial No. 09/652,324, filed August 31, 2000, ~~Attorney Docket No. 1662-29600~~; "Mechanism To Reorder Memory Read And Write Transactions For Reduced Latency And Increased Bandwidth," Serial No. 09/653,094, filed August 31, 2000, ~~Attorney Docket No. 1662-30800~~; "Look Ahead Mechanism To System For Minimizing Memory And Manage Bank Conflicts In A Computer Memory System," Serial No. 09/652,325, filed August 31, 2000, ~~Attorney Docket No. 1662-30900~~; "Computer Resource Management And Allocation System Scheme That Ensures Forward Progress, Maximizes Utilization Of Available Buffers And Guarantees Minimum Request Rate," Serial No. 09/651,945, filed August 31, 2000, ~~Attorney Docket No. 1662-31000~~; "Input Data Recovery Scheme," Serial No. 09/653,643, filed August 31, 2000, ~~Attorney Docket No. 1662-31100~~; "Fast Lane Prefetching," Serial No. 09/652,451, filed August 31, 2000, ~~Attorney Docket No. 1662-31200~~; "Mechanism For Synchronizing Multiple Skewed Source-Synchronous Data Channels With Automatic Initialization Feature," Serial No. 09/652,480, filed August 31, 2000, ~~Attorney Docket No. 1662-31300~~; and "Chaining Directory Reads And Writes To Reduce DRAM Bandwidth In A Directory Based CC-NUMA Protocol," Serial No. 09/652,315, filed

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 | August 31, 2000, ~~Attorney Docket No. 1662-31500~~, all of which are incorporated by reference herein.
